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Attorney Docket No. 5649-1202

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Ahn et al.

U.S. Patent No.: 7,282,761 B2

Application No.: 10/696,615

Issued: October 16, 2007

Filed: October 28, 2003

For: SEMICONDUCTOR MEMORY DEVICES HAVING OFFSET TRANSISTORS AND
METHODS OF FABRICATING THE SAME

October 31, 2007

Commissioner for Patents

Att: Certificate of Correction Branch

P.O. Box 1450

Alexandria, VA 22313-1450

**ERRORS APPEARING IN PRINTED PAGES
OF THE LETTERS PATENT**

Sir:

It is noted that certain errors appear in the Letters Patent identified above as

follows:

In Brief Description of the Drawings:

Column 4, Line 20: "embodimentss" should read --embodiments--

The above error is a mistake incurred through the fault of the Assignee, but does not appear to merit a Certificate of Correction. It is respectfully requested, however, that this letter be placed on record in the file of this patent so that no question may arise as to the true meaning of the particular terms mentioned above as they appear in the patent.

Respectfully submitted,

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Certificate

NOV 07 2007

of Correction

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450, on October 31, 2007.

Jessica M. French

Jessica M. French

NOV 7 2007



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**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER
No. 35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof, and a return post card. Applicants further direct the Commissioner's attention to Amendment dated January 11, 2005.

Respectfully submitted,

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Jessica M. French

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,282,761 B2
DATED : October 16, 2007
INVENTOR(S) : Ahn et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Cover Page:

Sec. (54): Should read --SEMICONDUCTOR MEMORY DEVICES
HAVING OFFSET TRANSISTORS--

Column 1, lines 1-3: Please correct title to read --SEMICONDUCTOR
MEMORY DEVICES HAVING OFFSET
TRANSISTORS--

MAILING ADDRESS OF SENDER:
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Raleigh, NC 27627

PATENT NO. 7,282,761 B2
No. of additional copies: 1

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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